



S/N 10/585,680

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Xiaodan Jiang et al.	Examiner: Jason D. Mitchell
Serial No.:	10/585,680	Group Art Unit: 2193
Filed:	July 10, 2006	Docket No.: P22886/INT.P036
Title:	METHOD AND APPARATUS FOR PARTITIONING PROGRAMS TO BALANCE MEMORY LATENCY	

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action mailed April 12, 2011, Applicants respectfully request the Examiner to consider the following response. A request for continued examination is being submitted herewith.